



C of C
EOW

PATENT
Docket No. SPLX.P0093

CERTIFICATE OF MAILING BY "FIRST CLASS MAIL"

I hereby certify that this correspondence is being deposited with the United States Postal Service as first class mail in an envelope addressed to: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450, on April 7, 2005.

Mani Adeli

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In the application of:

Steven Teig

Serial No.: 10/062,044

Patent No.: 6,857,117

Filing Date: 01/31/2002

For: METHOD AND APPARATUS FOR
PRODUCING A CIRCUIT DESCRIPTION OF
A DESIGN

Examiner: Naum B Levin

Group Art: 2825

TRANSMITTAL LETTER FOR REQUEST FOR CERTIFICATE OF CORRECTION

Attention Certificate of Corrections Branch
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Dear Sir:

Attached, please, find a Request for Certificate of Correction on PTO/SB/44 Form. There is no fee associated with this Request, as the corrected errors are not the fault of the Applicant. However, in the unlikely event that the Patent Office determines that additional fee is required, Applicant authorizes the Assistant Commissioner to charge the cost of such fees to Deposit Account No. 50-1128 referencing SPLX.P0093.

Dated: April 7, 2005

Respectfully submitted,

By: _____

Mari Adeli

Registration No. 39,585

Stattler, Johansen & Adeli LLP
1875 Century Park East, Suite 1050
Los Angeles, CA 90067

Phone No. (310) 785-0140 x301
Fax No. (310) 785-9558

**UNITED STATES PATENT AND TRADEMARK OFFICE
CERTIFICATE OF CORRECTION**

PATENT NO.: 6,857,117 B2

DATED : February 15, 2005

INVENTOR(S) : Teig et al.

It is certified that errors appear in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

In the claims, column 44, line 24, after "wherein the set of circuit element includes only one circuit element." should read --wherein the set of circuit elements includes only one circuit element.--

In the claims, column 44, line 26, after "wherein the set of circuit element includes at least two circuit elements." should read --wherein the set of circuit elements includes at least two circuit elements.--

In the claims, column 44, line 49, after "and the BDD of a second output function is derived from the BDD's of at least th first and second local functions." should read --and the BDD of a second output function is derived from the BDD's of at least the first and second local functions.-

In the claims, column 45, line 56, after "for each particular index pair formed by the primary index and on of the secondary indices," should read --for each particular index pair formed by the primary index and one of the secondary indices,--

In the claims, column 46, line 27, after "a candidate sub-network that performs a at least the output Boolean functions," should read --a candidate sub-network that performs at least the output Boolean functions,--

In the claims, column 47, line 1, after "identifying each replacement subnetwork stored in the storage structure that is associated with each index in the set of indices;" should read --identifying each replacement sub-network stored in the storage structure that is associated with each index in the set of indices;--

In the claims, column 48, line 6, after “associate with all index pairs.” should read --associated with all index pairs.--

In the claims, column 48, line 26, after “ replacement fit sub-network in the design.” Should read --replacement sub-network in the design.--

In the claims, column 48, line 28, after “wherein the candidate sub-network has set of input variables,” should read --wherein the candidate sub-network has a set of input variables,--

In the claims, column 48, line 45, after “wherein identifying the canonic representation includes identifying canonic representation of the first function.” should read --wherein identifying the canonic representation includes identifying a canonic representation of the first function.--

In the claims, column 48, line 46, after “The method of claim 44 further comprising condensing the canonic representation to obtain a condensed representation the first function.” should read --The method of claim 44 further comprising condensing the canonic representation to obtain a condensed representation of the first function.--

MAILING ADDRESS OF SENDER: Stattler, Johansen & Adeli
1875 Century Park East, Suite 1050
Los Angeles, CA 90067

PATENT NO. 6,857,117 B2

No. of additional copies



This collection of information is required by 37 CFR 1.322, 1.323, and 1.324. The information is required to obtain or retain a benefit by the public which is to file (and by the USPTO to process) an application. Confidentiality is governed by 35 U.S.C. 122 and 37 CFR 1.14. This collection is estimated to take 1.0 hour to complete, including gathering, preparing, and submitting the completed application form to the USPTO. Time will vary depending upon the individual case. Any comments on the amount of time you require to complete this form and/or suggestions for reducing this burden, should be sent to the Chief Information Officer, U.S. Patent and Trademark Office, U.S. Department of Commerce, P.O. Box 1450, Alexandria, VA 22313-1450. DO NOT SEND FEES OR COMPLETED FORMS TO THIS ADDRESS. SEND TO: **Attention Certificate of Corrections Branch, Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.**